

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Hari K. Ravichandran
Assignee: Sun Microsystems, Inc.
Title: Apparatus and Method for Processor Performance Monitoring
Serial No.: 10/056,244 Filing Date: January 22, 2002
Examiner: Aaron D. Matthew Group Art Unit: 2114
Docket No.: P2678 Customer No.: 33438

Austin, Texas
May 2, 2005

Commissioner for Patents
PO Box 1450
Alexandria, Virginia 22313-1450

RESPONSE TO NON-FINAL OFFICE ACTION

Dear Sir:

This paper is responsive to the Office action dated March 1, 2005, having a shortened statutory period expiring June 1, 2005. Further examination and reconsideration are respectfully requested in view of the amendments and remarks set forth below.

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